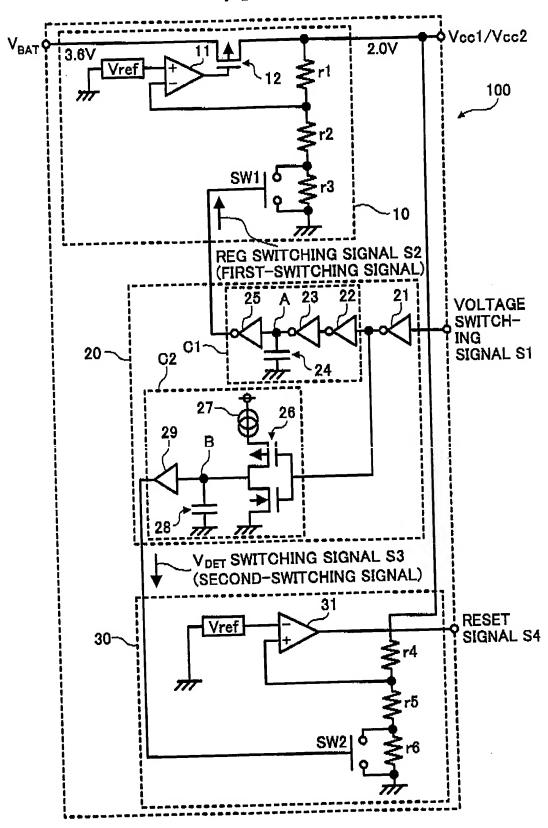
Title: POWER SUPPLY SYSTEM AND METHOD FOR SUPPLYING POWER TO CPU PROVIDING POWER SAVING MODE

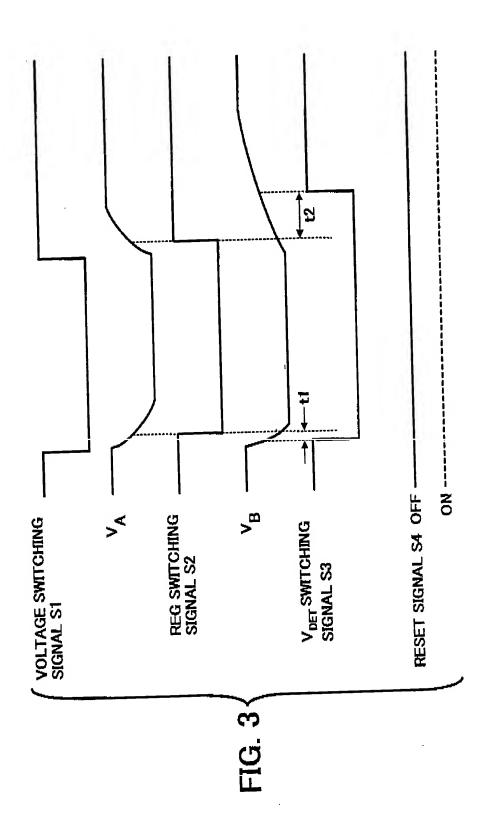
Sheet 1 of 6 Sheet 1 of 6 OPERATION KEY RECEPTION SECTION CPU 당 RESET SIGNAL S4 VOLTAGE SWITCHING SIGNAL S1 Vcc1/Vcc2 <u>0</u>-VOLTAGE DETECTING SECTION CONTROL 83 \$2 REG 20-9 VBAT -

Title: POWER SUPPLY SYSTEM AND METHOD FOR SUPPLYING POWER TO CPU PROVIDING POWER SAVING MODE

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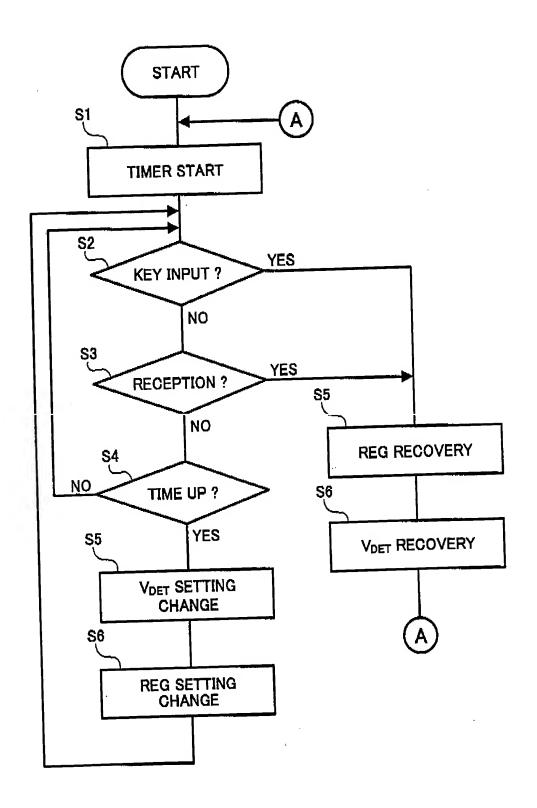
FIG. 2





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FIG. 5



Title: POWER SUPPLY SYSTEM AND METHOD FOR SUPPLYING POWER TO CPU PROVIDING POWER SAVING MODE

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